

Figure 1 Prior Art

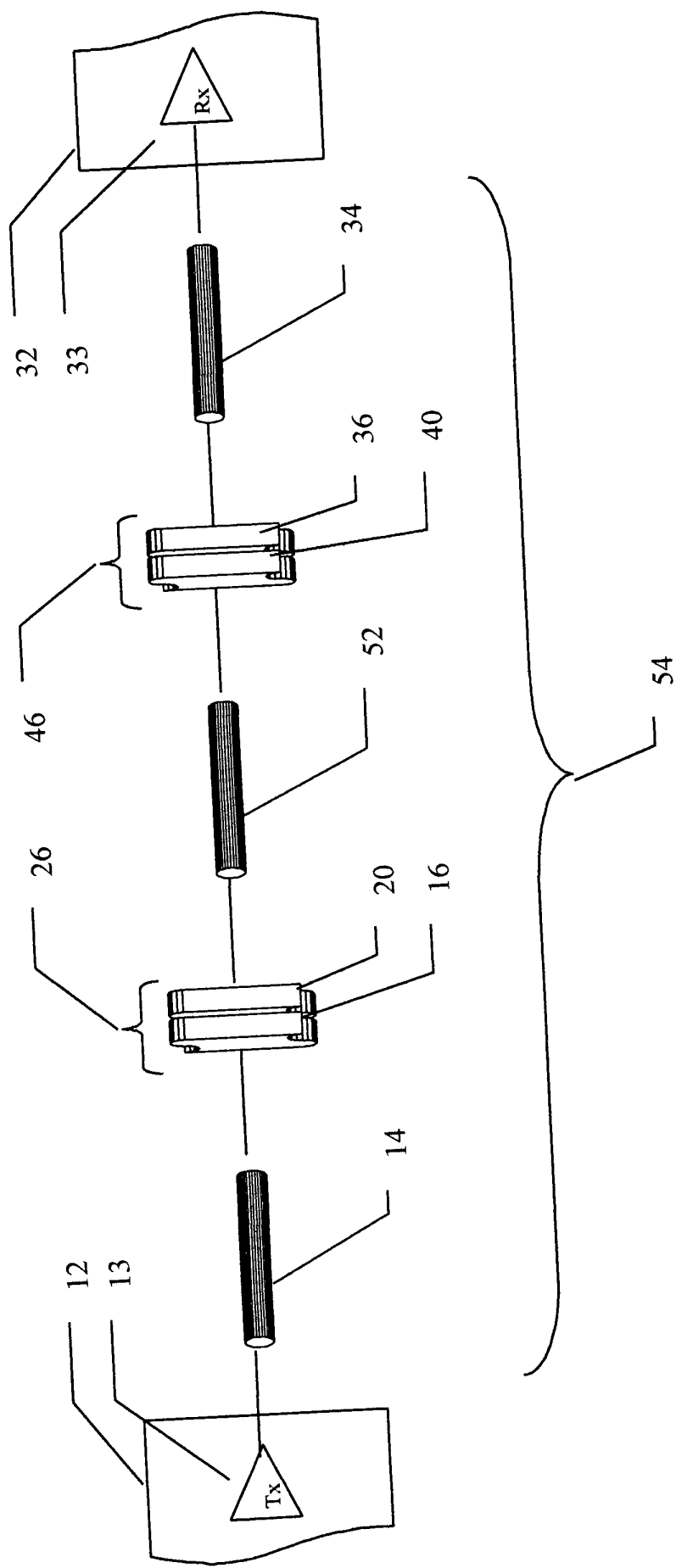


Figure 2 Prior Art

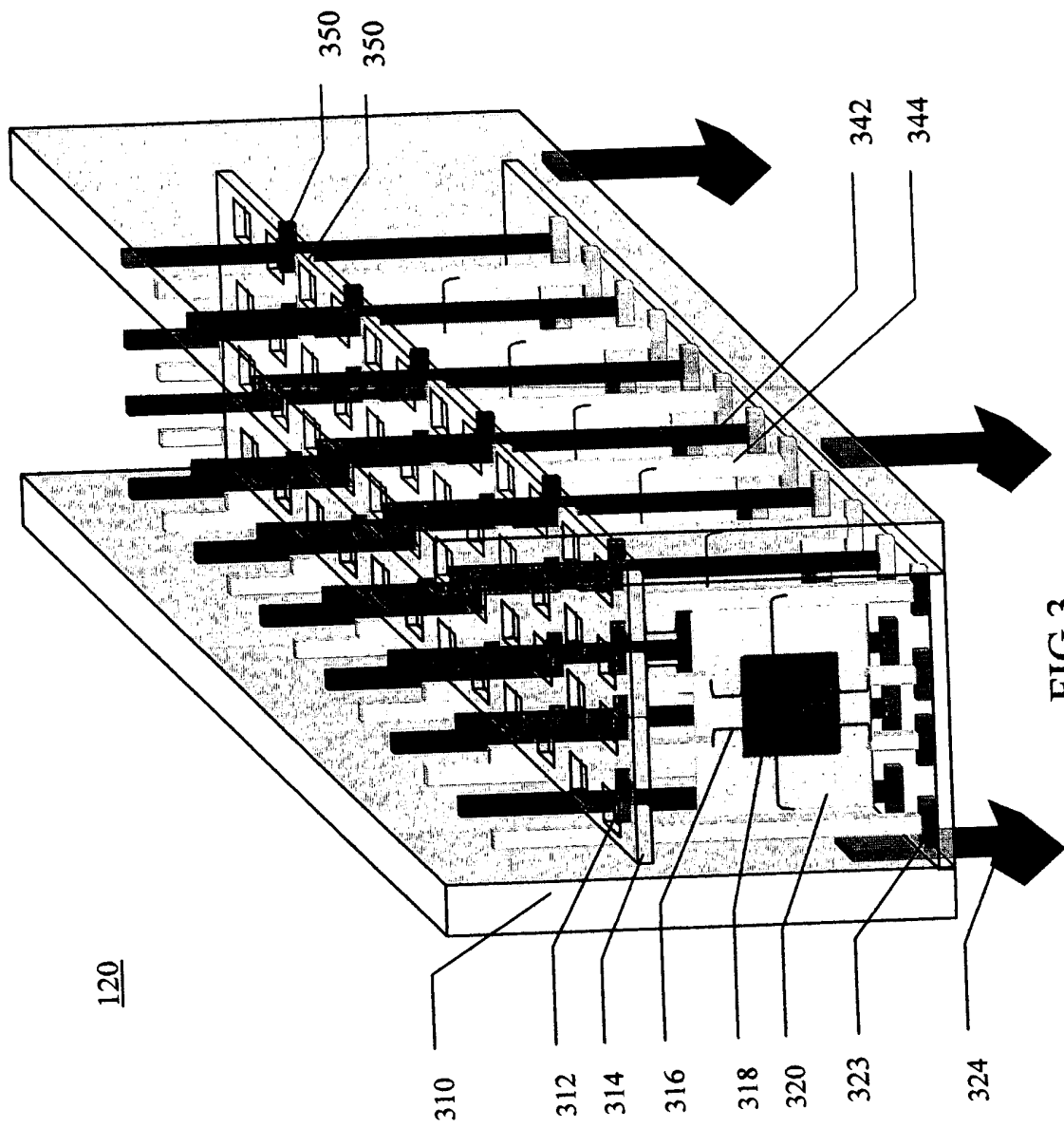


FIG 3

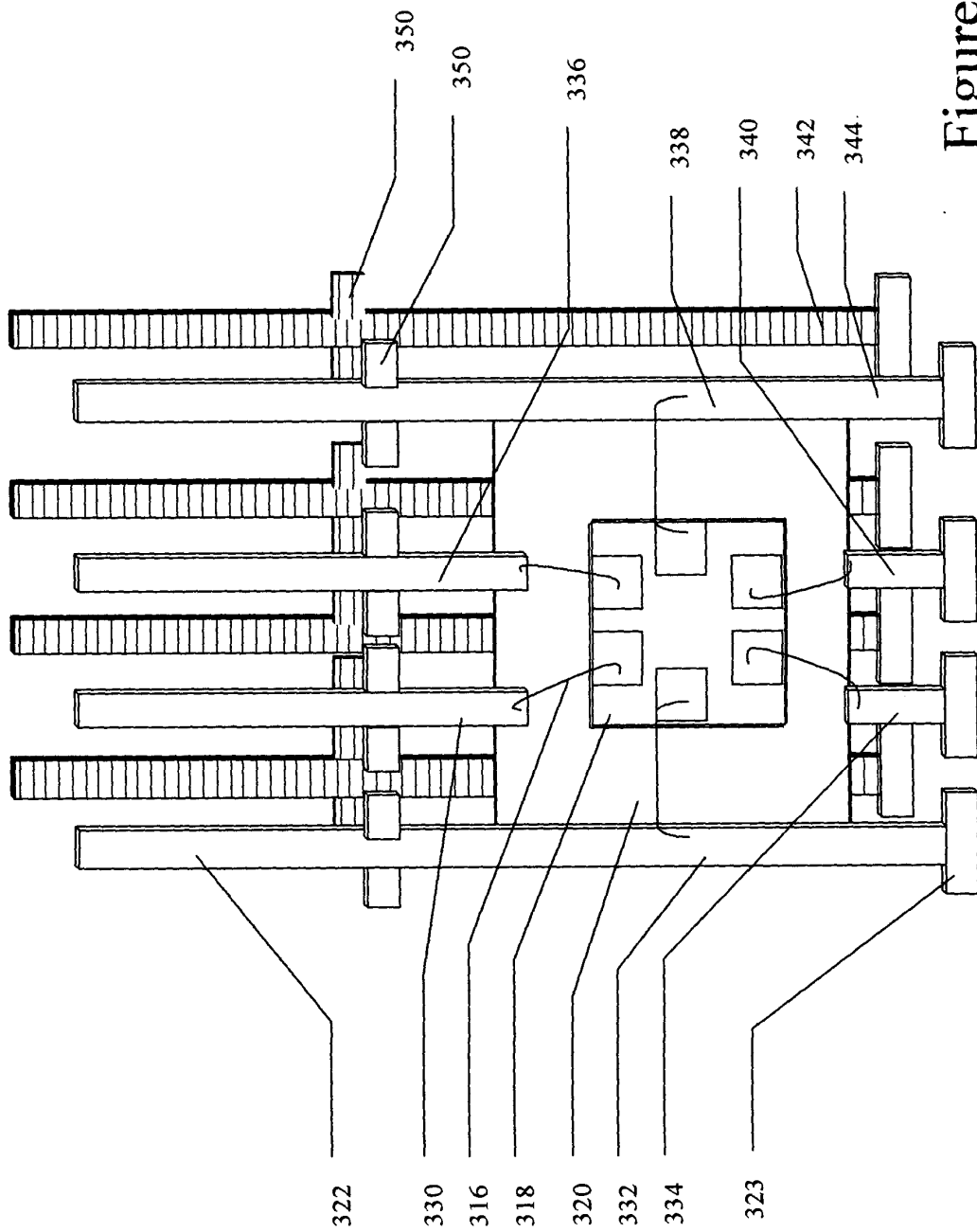


Figure 4

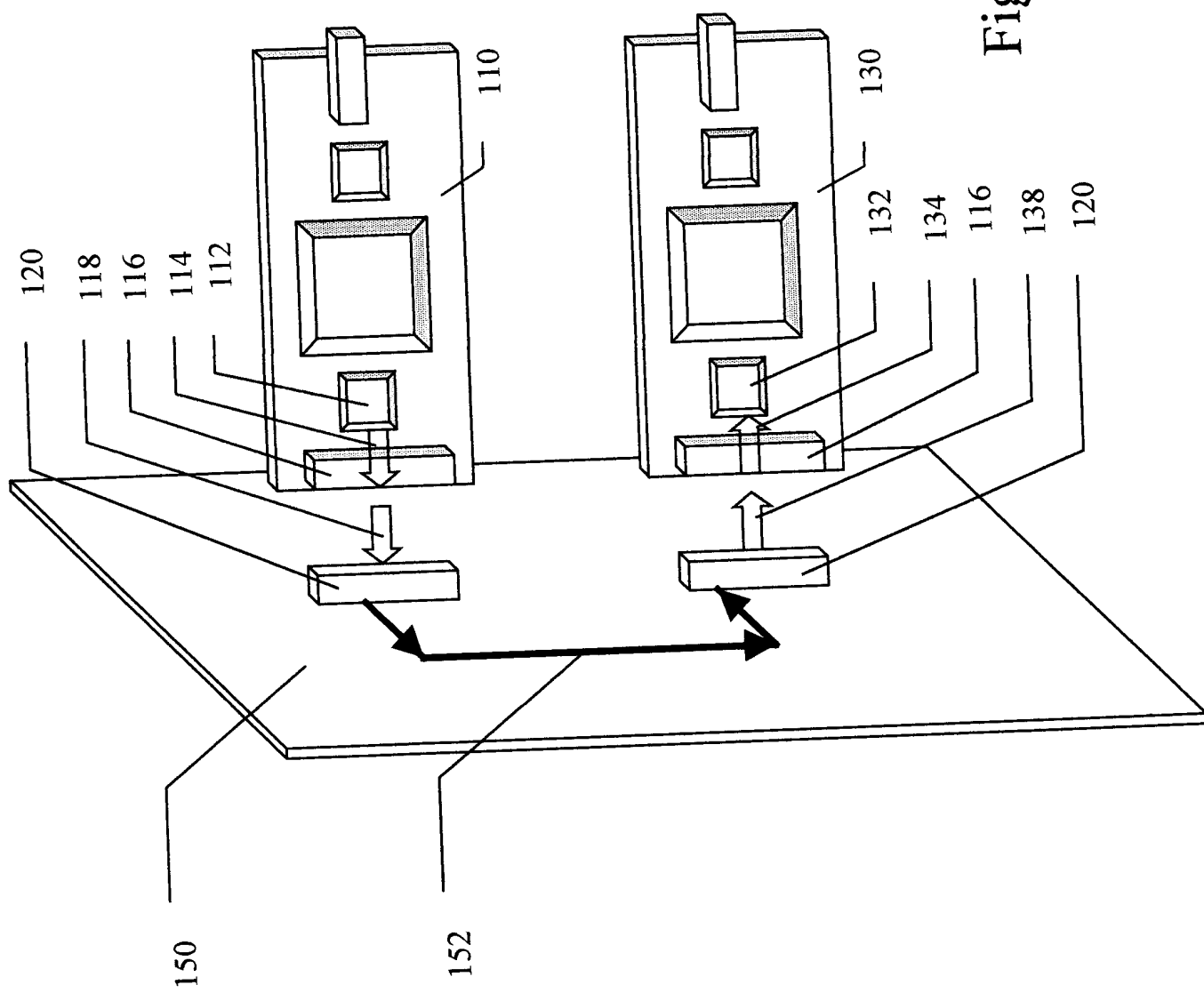


Figure 5

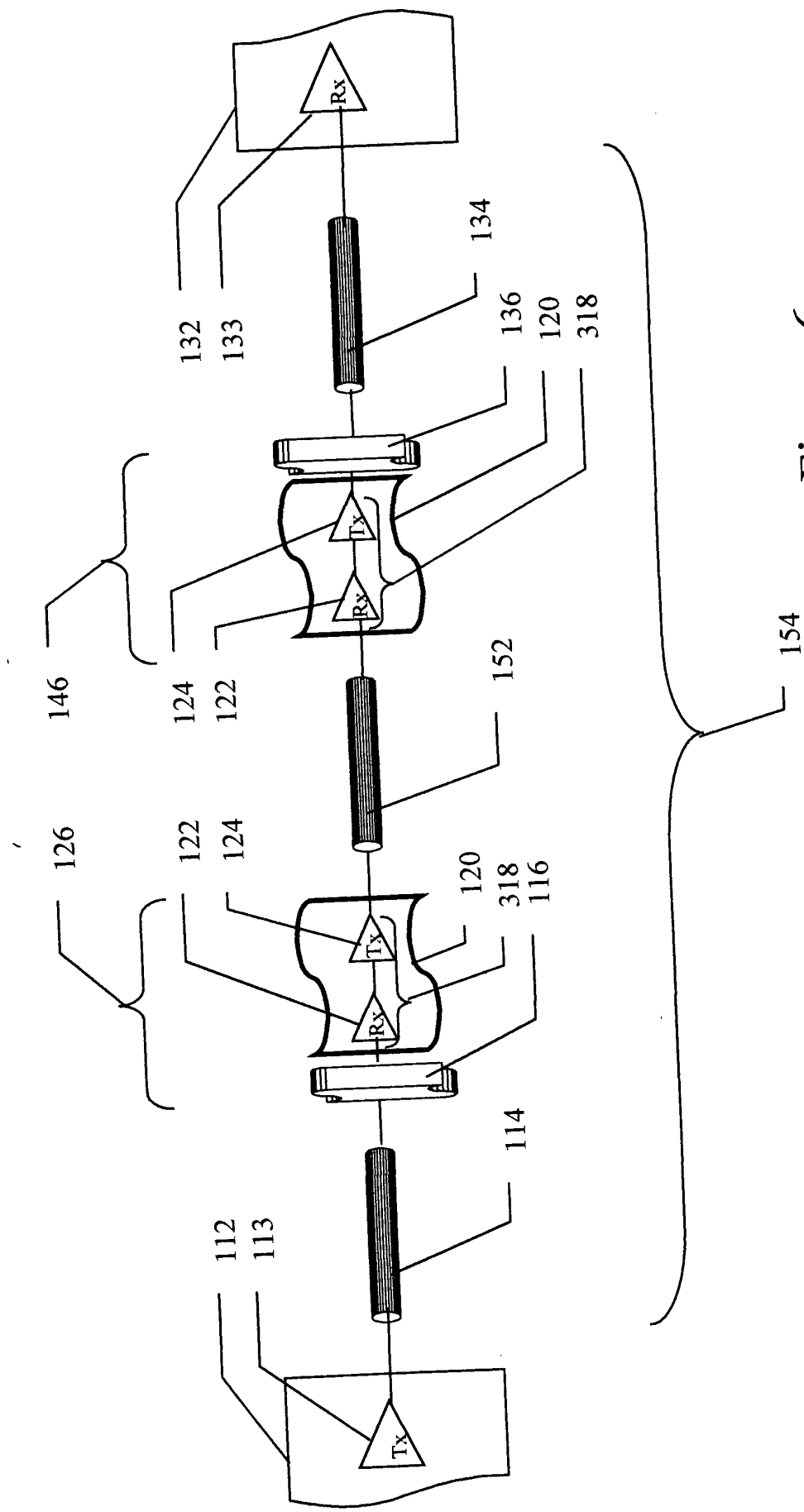


Figure 6

IC mounted on Pin subassembly

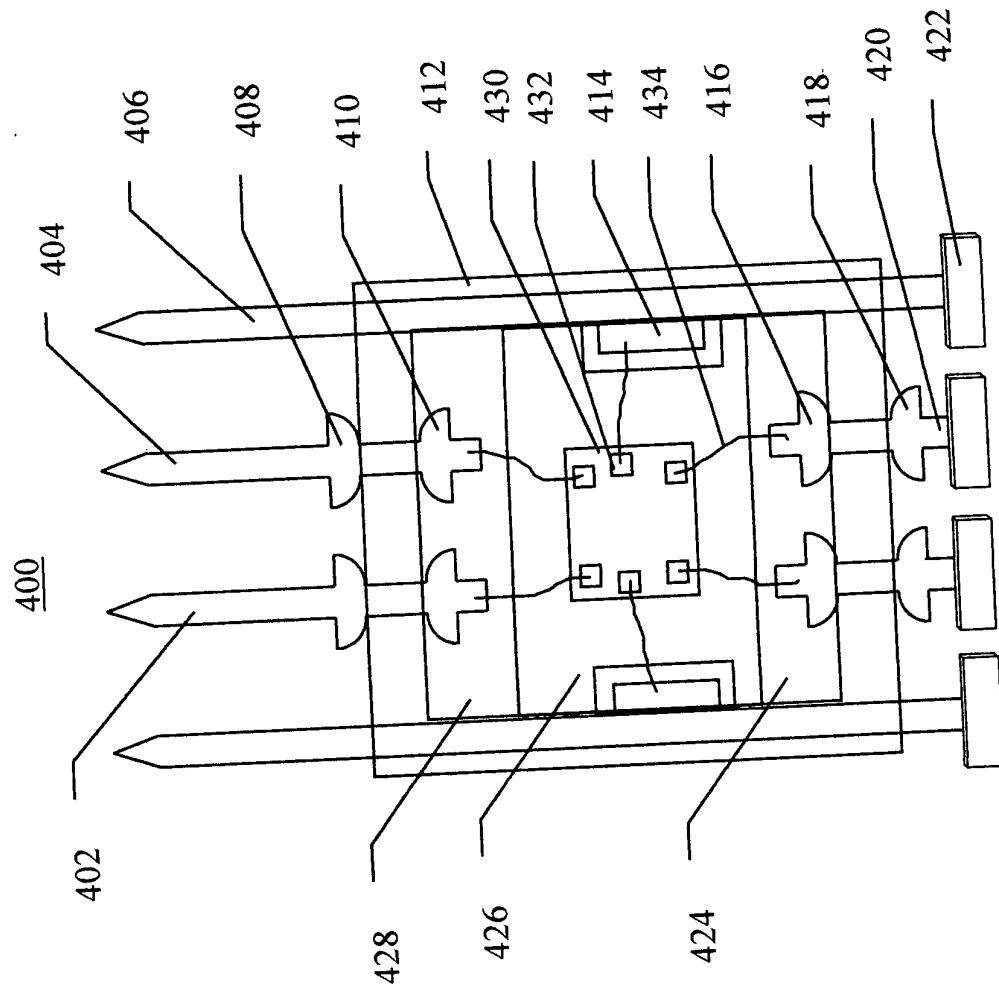


Figure 7

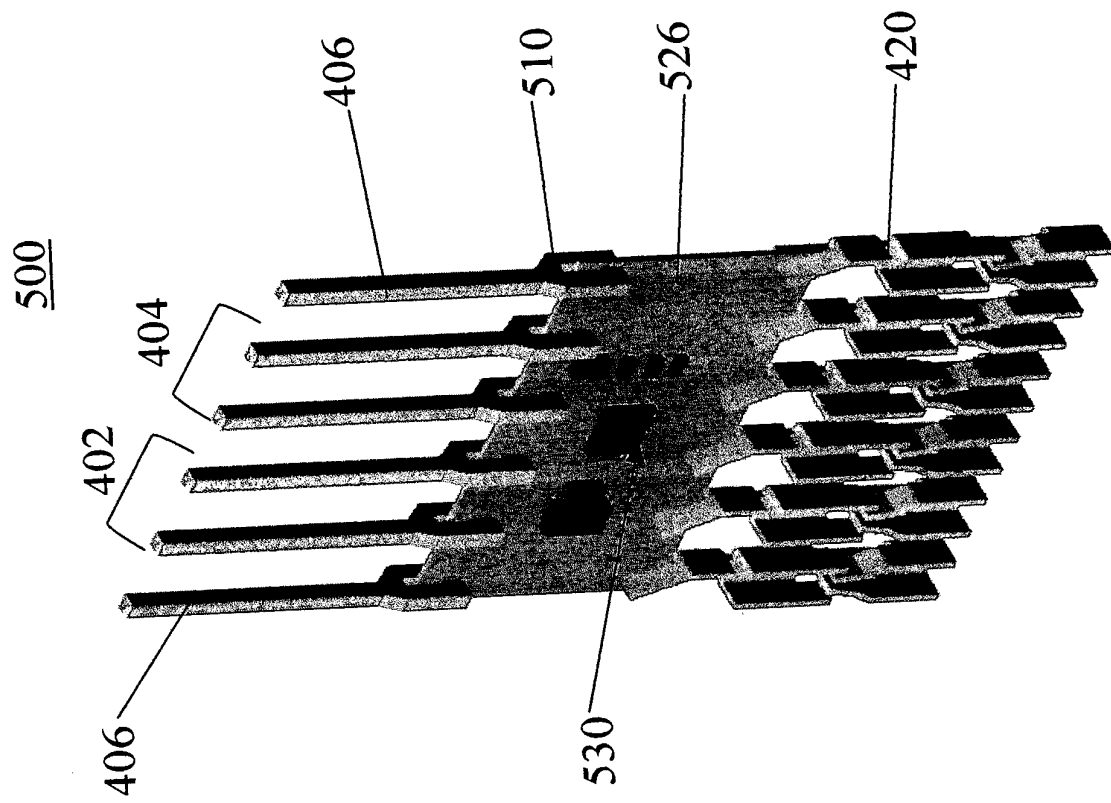


FIG 8

